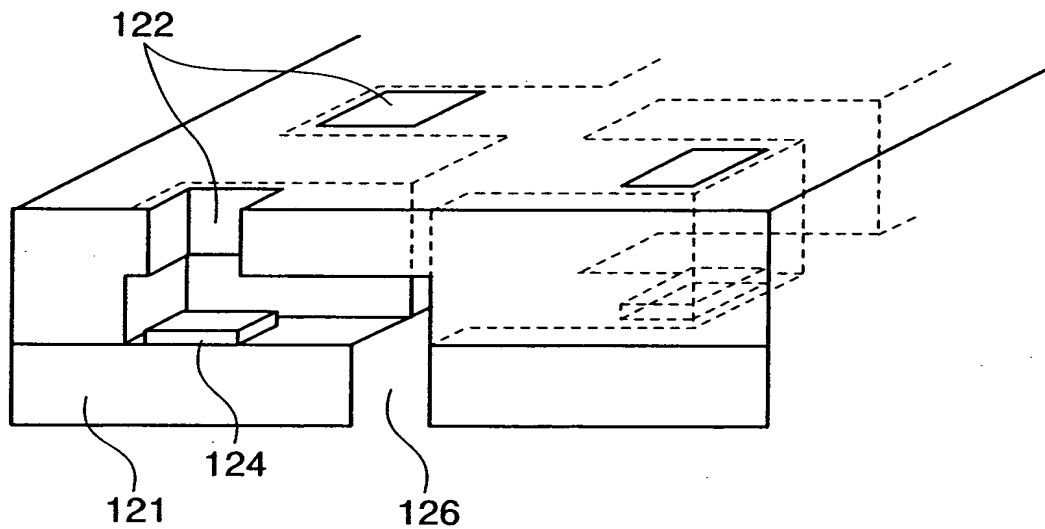


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FIG. 1B

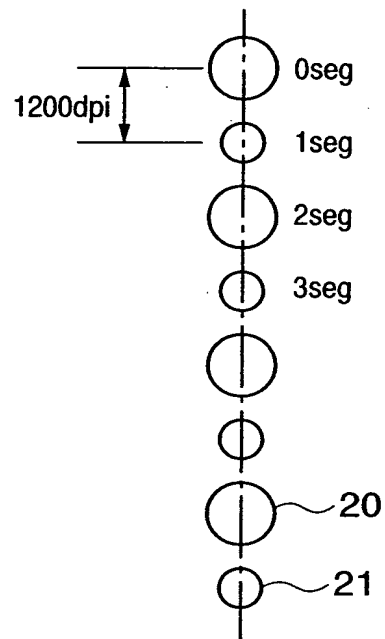


Replacement Sheet

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FIG. 2

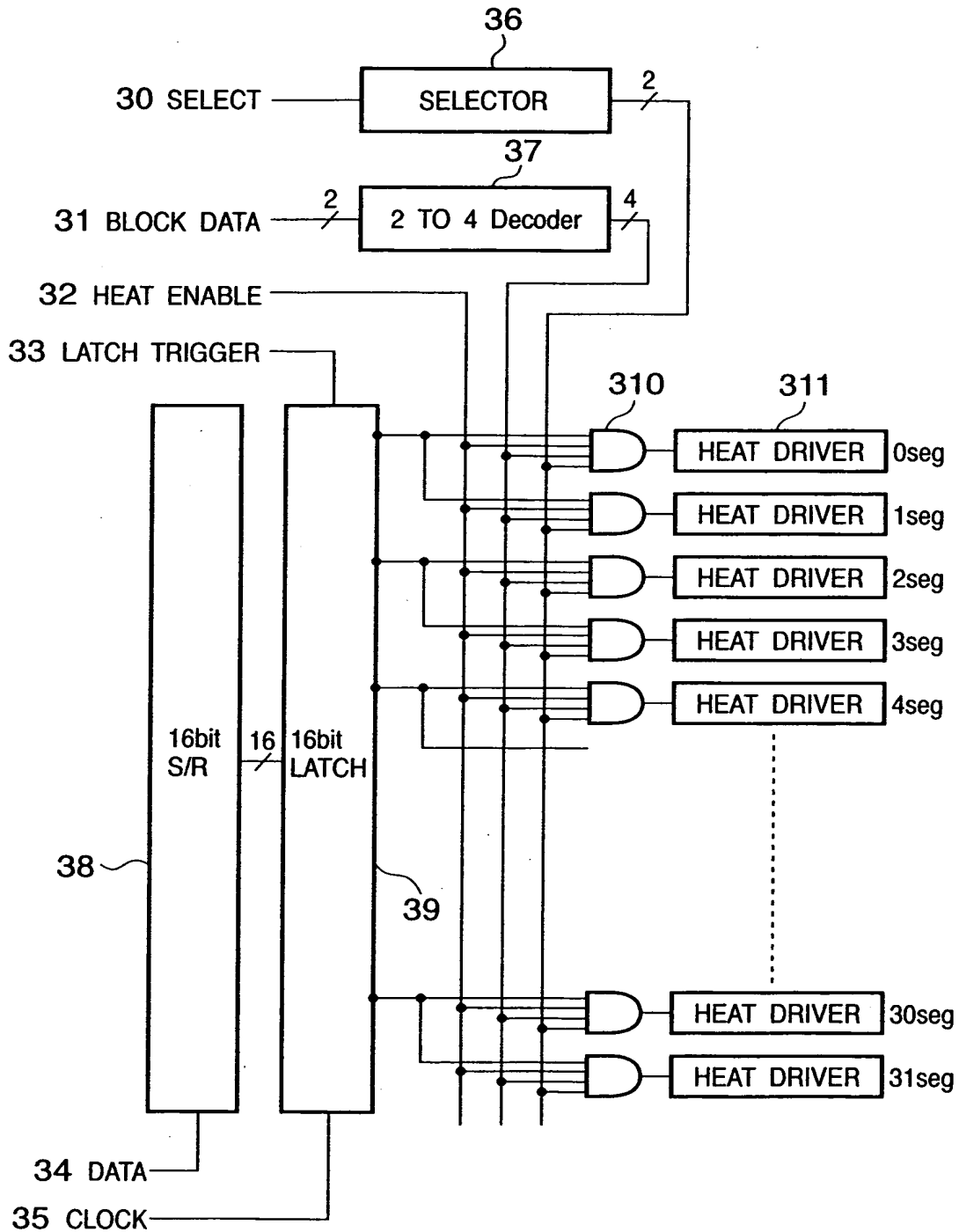


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FIG. 3



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FIG. 4

2 TO 4 INPUT		2 TO 4 Decoder OUTPUT			
BE0	BE1	BLE0	BLE1	BLE2	BLE3
L	L	H	L	L	L
H	L	L	H	L	L
L	H	L	L	H	L
H	H	L	L	L	H

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FIG. 5

SELECTOR INPUT	SELECTOR OUTPUT	
SELECT	SEL0	SEL1
L	H	L
H	L	H

Replacement Sheet

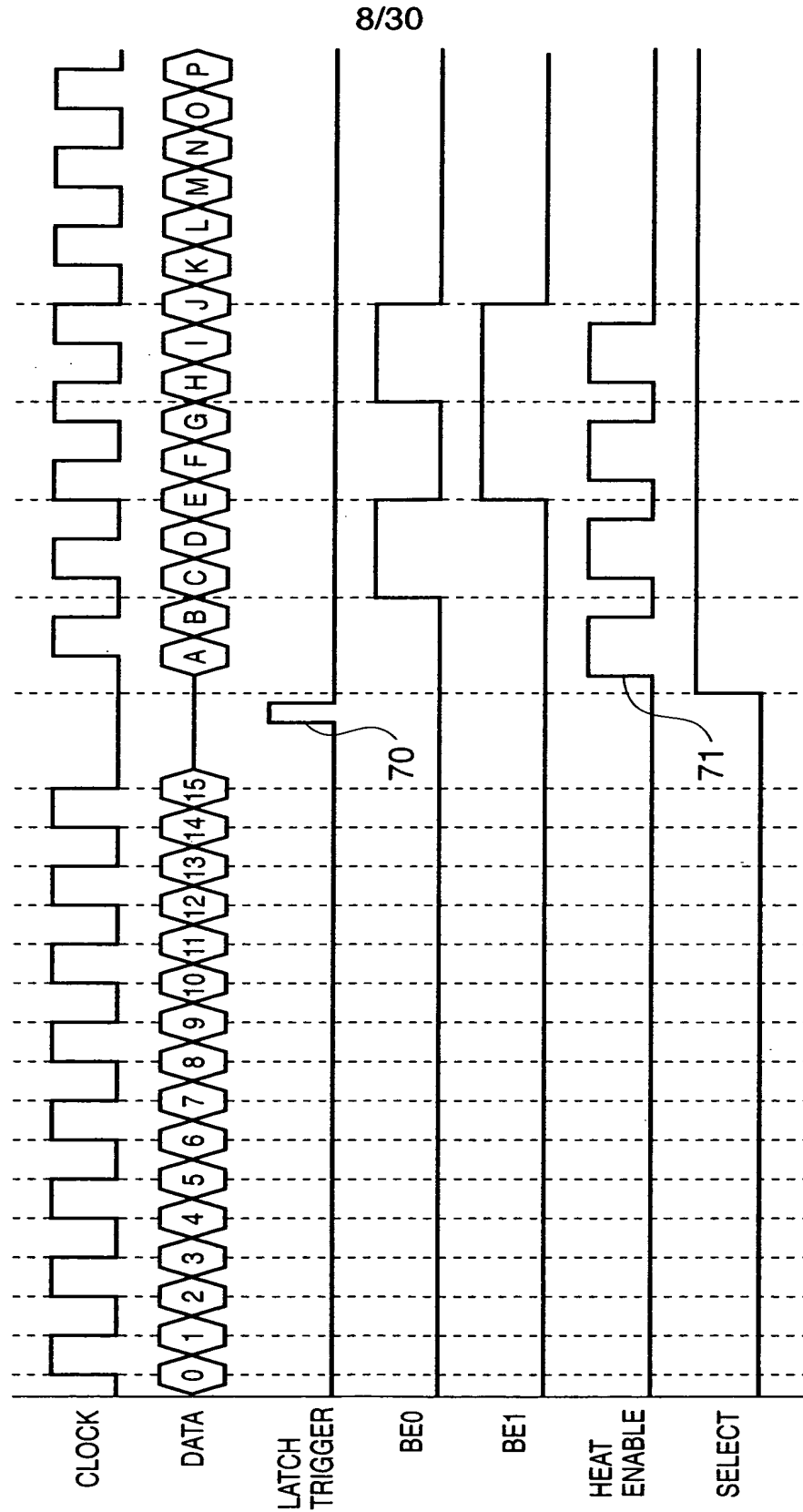
INVENTOR: Yoshinori Nakajima, et al.
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FIG. 6

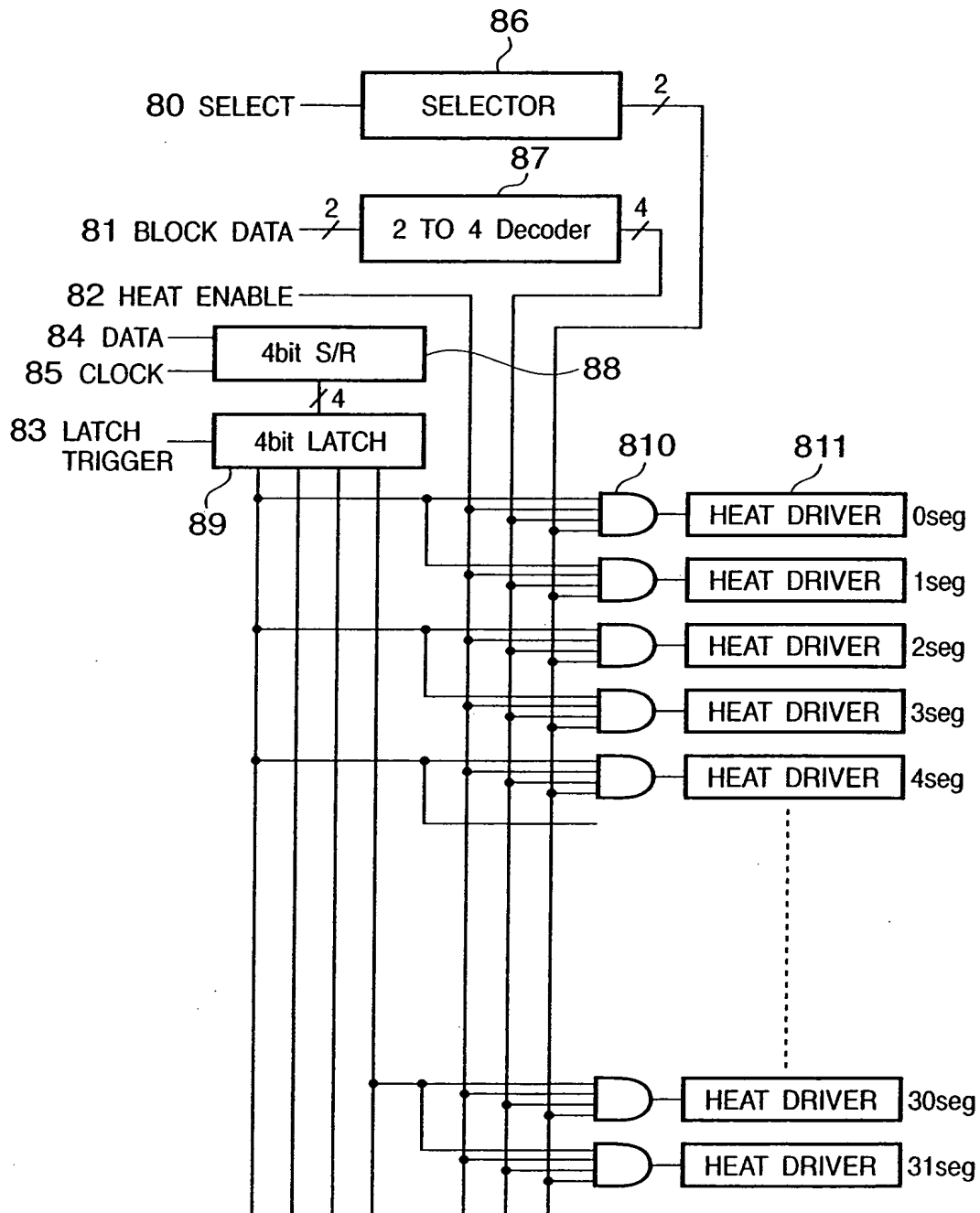
seg	BLE	SEL
0	0	0
1	0	1
2	1	0
3	1	1
4	2	0
5	2	1
6	3	0
7	3	1
8	0	0
9	0	1
10	1	0
11	1	1
12	2	0
13	2	1
14	3	0
15	3	1
16	0	0
17	0	1
18	1	0
19	1	1
20	2	0
21	2	1
22	3	0
23	3	1
24	0	0
25	0	1
26	1	0
27	1	1
28	2	0
29	2	1
30	3	0
31	3	1

FIG. 7



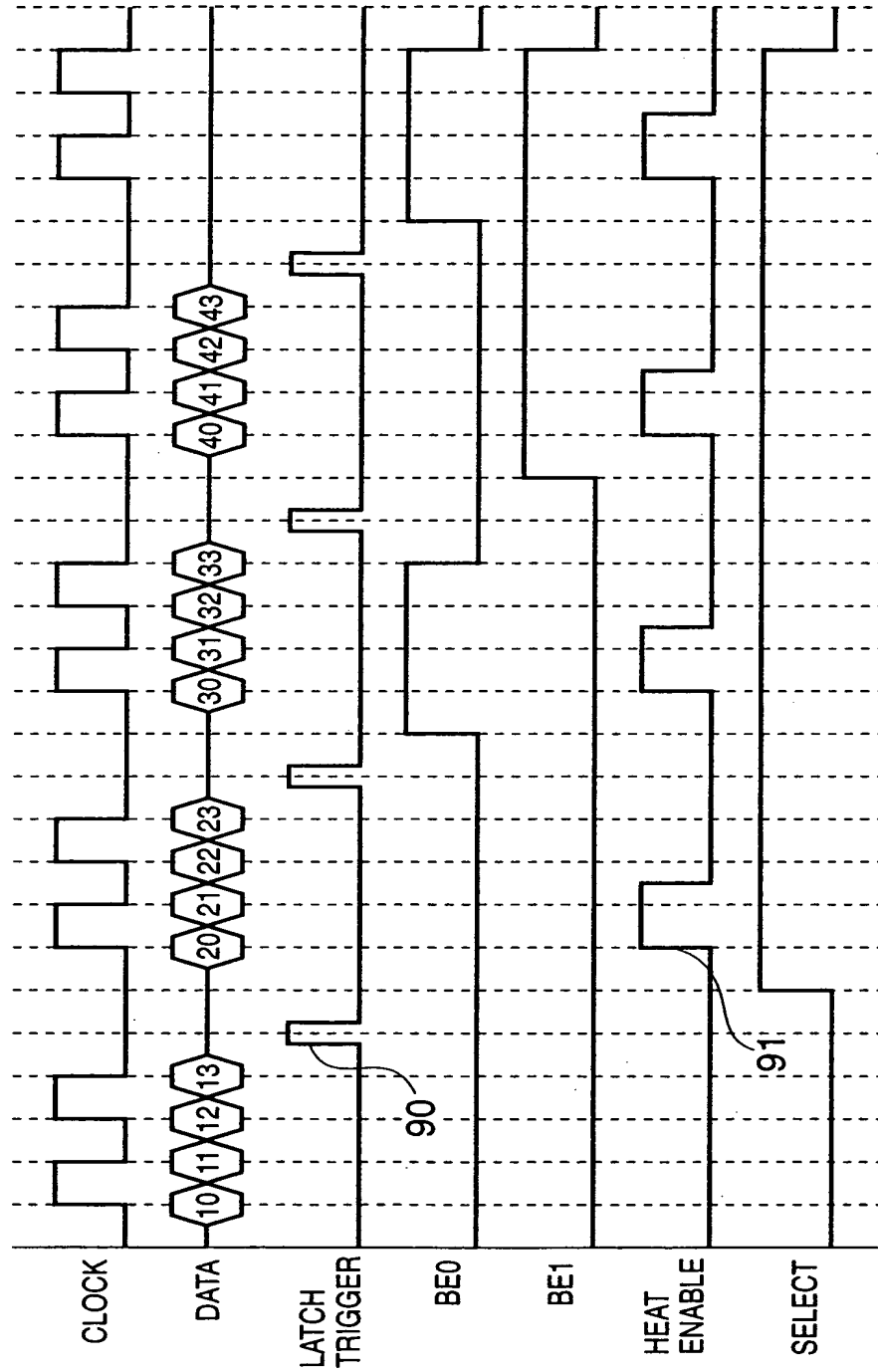
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FIG. 8



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FIG. 9



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FIG. 10

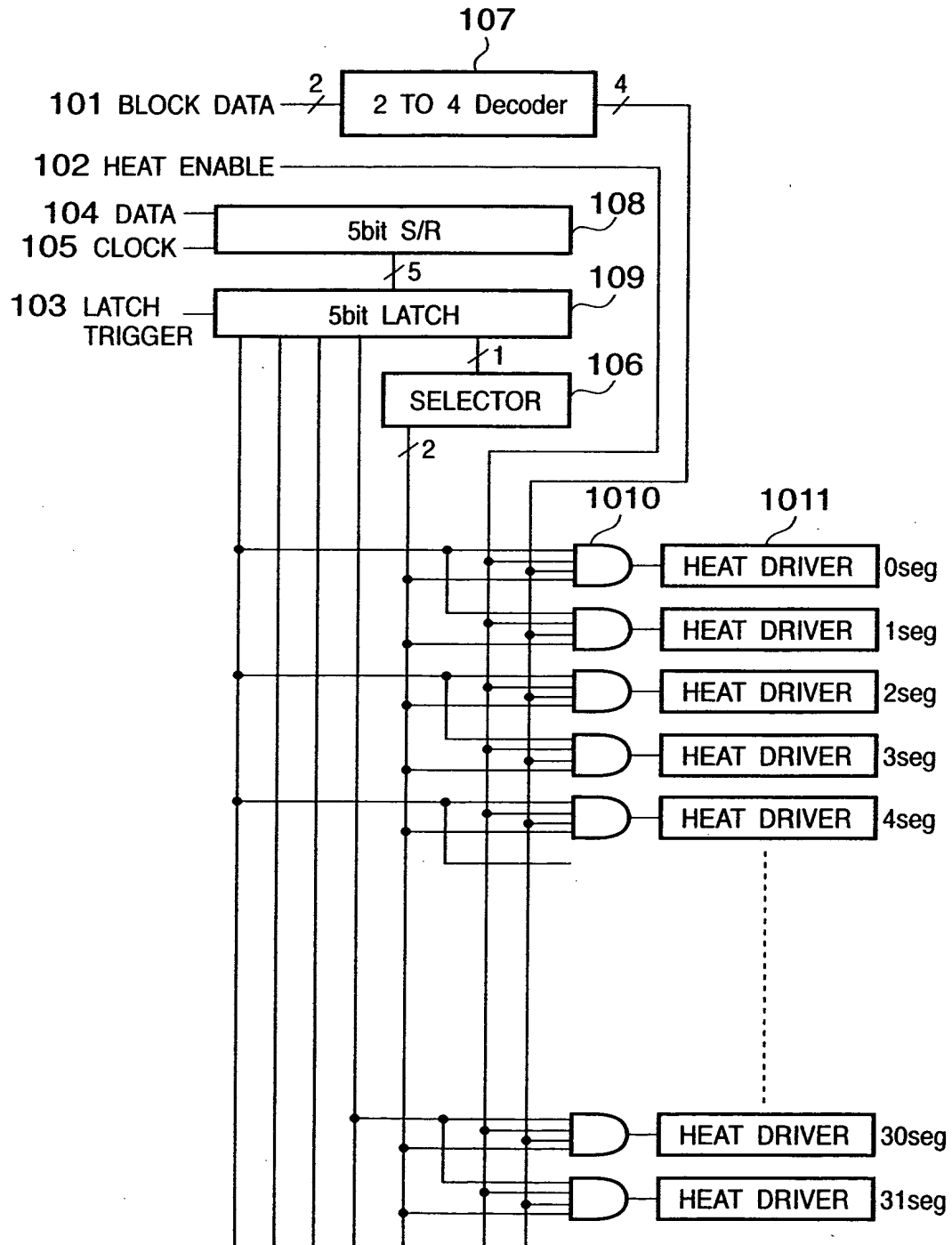
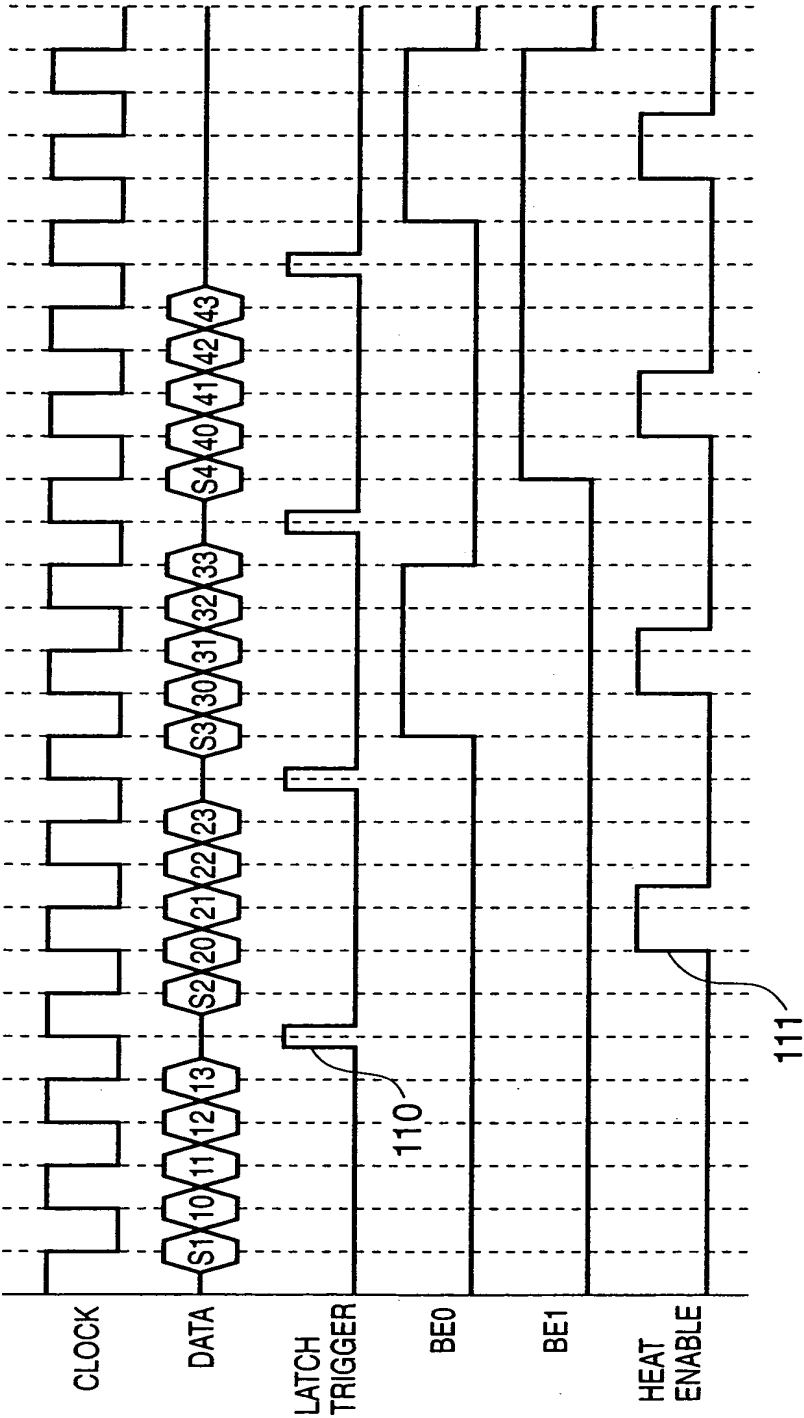


FIG. 11

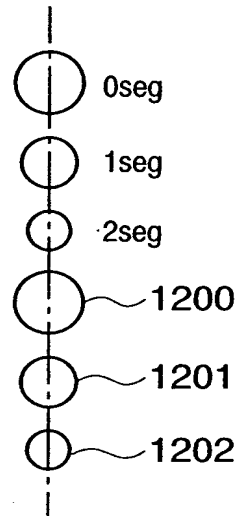


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FIG. 12



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FIG. 13

SELECTOR INPUT	SELECTOR OUTPUT		
SELECT	SEL0	SEL1	SEL2
LL	H	L	L
LH	L	H	L
HL	L	L	H

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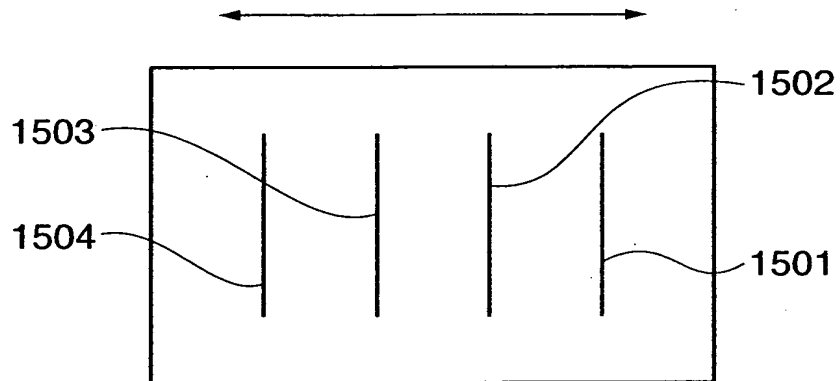
seg	BLE	SEL
0	0	0
1	0	1
2	0	2
3	1	0
4	1	1
5	1	2
6	2	0
7	2	1
8	2	2
9	3	0
10	3	1
11	3	2
12	0	0
13	0	1
14	0	2
15	1	0
16	1	1
17	1	2
18	2	0
19	2	1
20	2	2
21	3	0
22	3	1
23	3	2
24	0	0
25	0	1
26	0	2
27	1	0
28	1	1
29	1	2
30	2	0
31	2	1
32	2	2
33	3	0
34	3	1
35	3	2
36	0	0
37	0	1
38	0	2
39	1	0
40	1	1
41	1	2
42	2	0
43	2	1
44	2	2
45	3	0
46	3	1
47	3	2

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FIG. 15

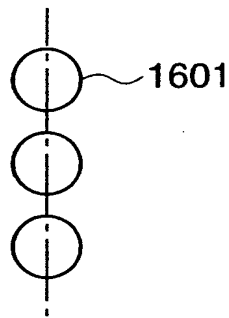


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FIG. 16



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FIG. 17

SIGNAL	BLACK	CYAN	MAGENTA	YELLOW
SELECT	Non	A1	A2	A3
BLOCK DATA	B	B	B	B
HEAT ENABLE	C1	C2	C2	C2
LATCH TRIGGER	D	D	D	D
DATA	E1	E2	E3	E4
CLOCK	F	F	F	F

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FIG. 18

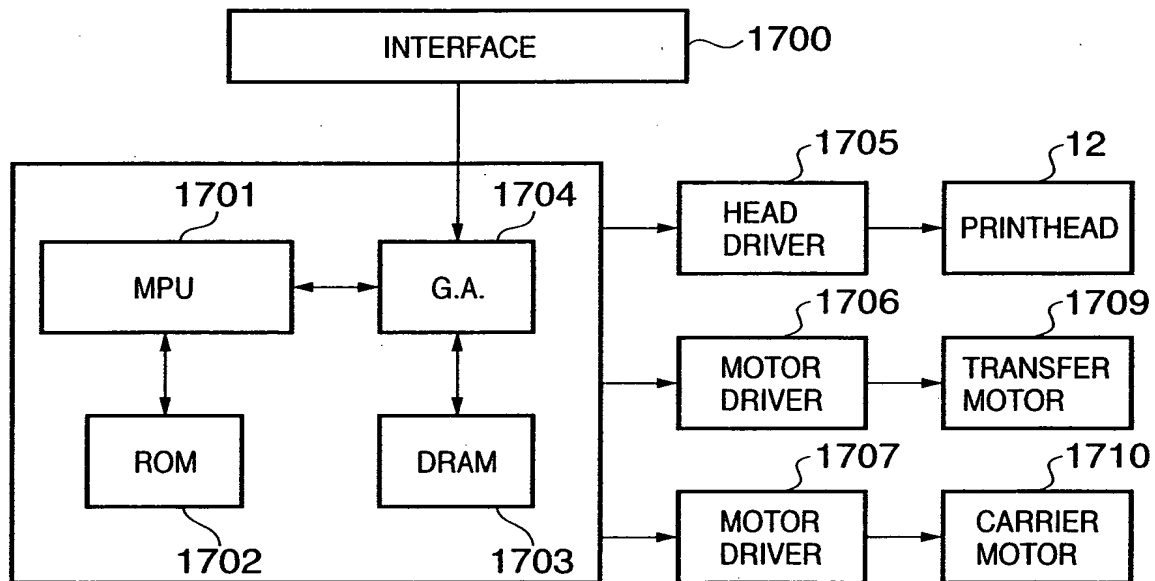
SIGNAL	BLACK	CYAN	MAGENTA	YELLOW
SELECT	Non	A	A	A
BLOCK DATA	B	B	B	B
HEAT ENABLE	C1	C2	C2	C2
LATCH TRIGGER	D	D	D	D
DATA	E1	E2	E3	E4
CLOCK	F	F	F	F

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FIG. 19

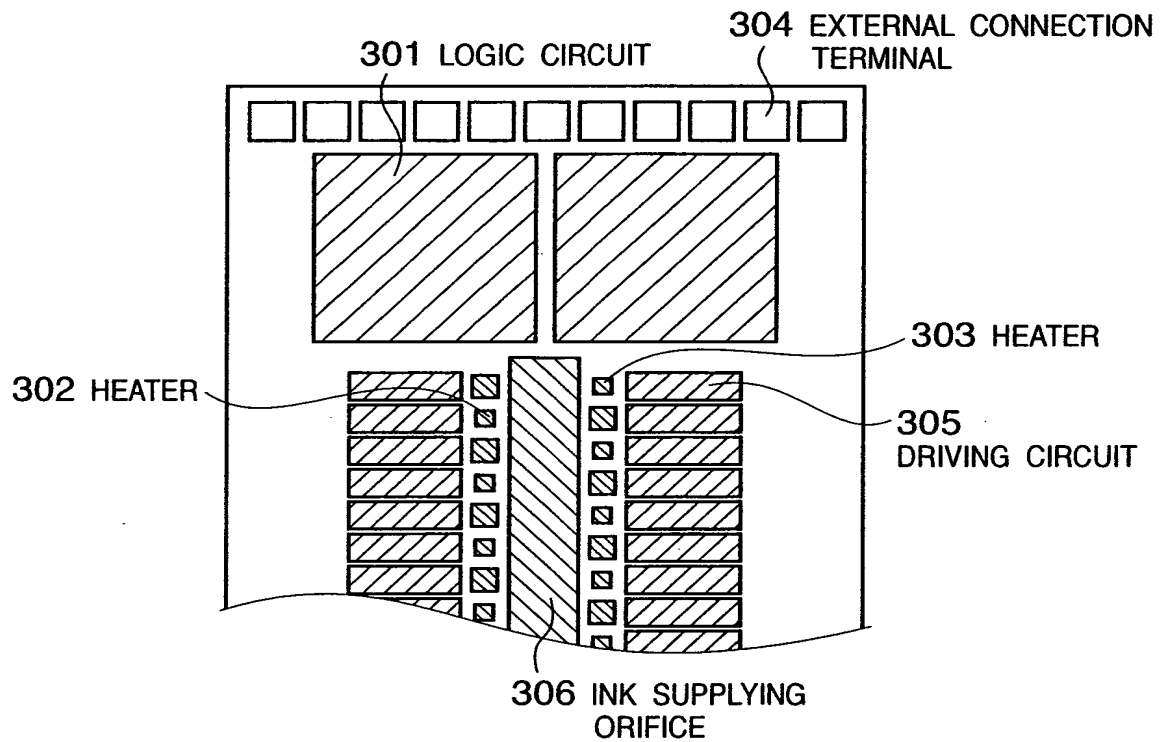


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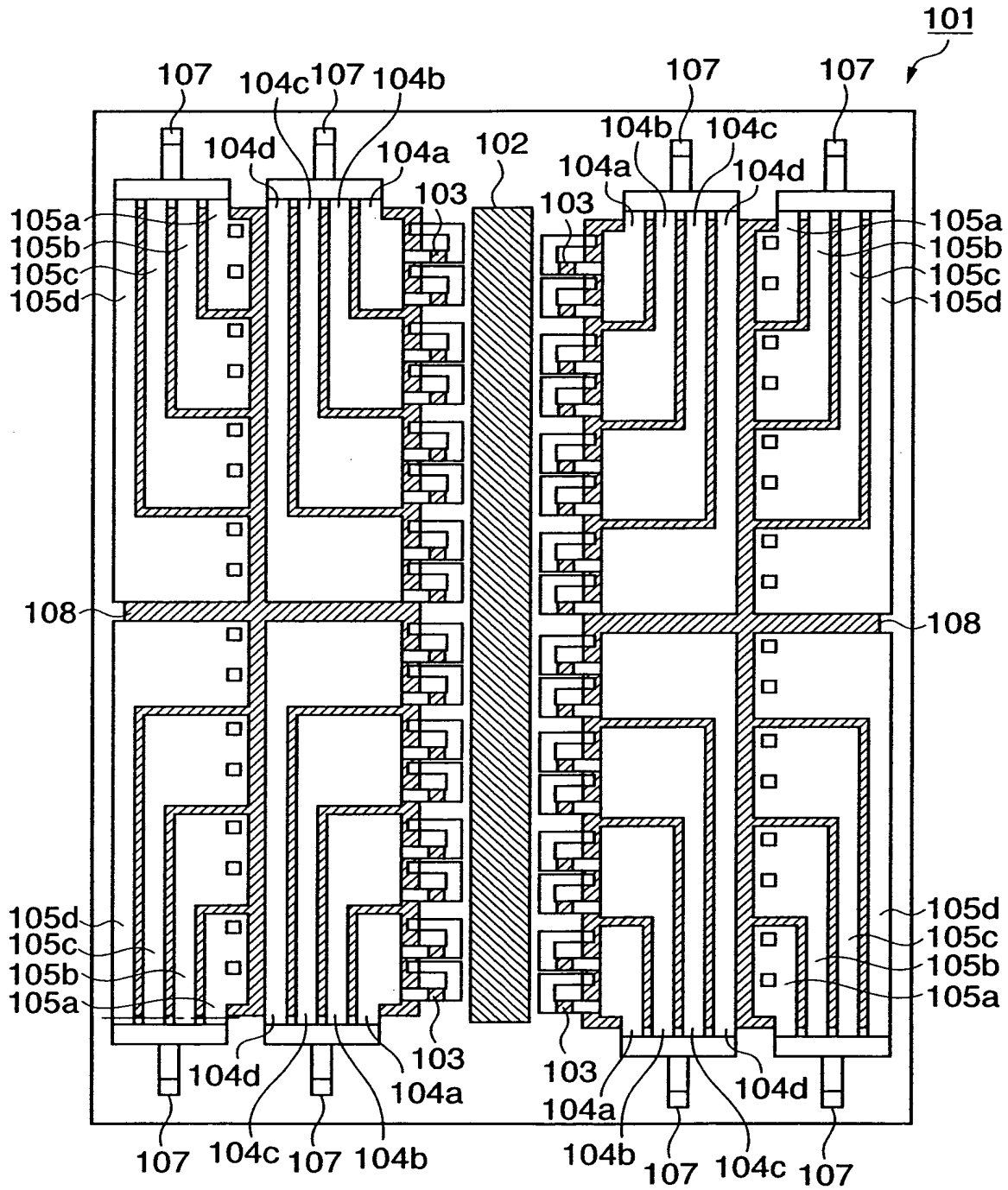
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FIG. 20



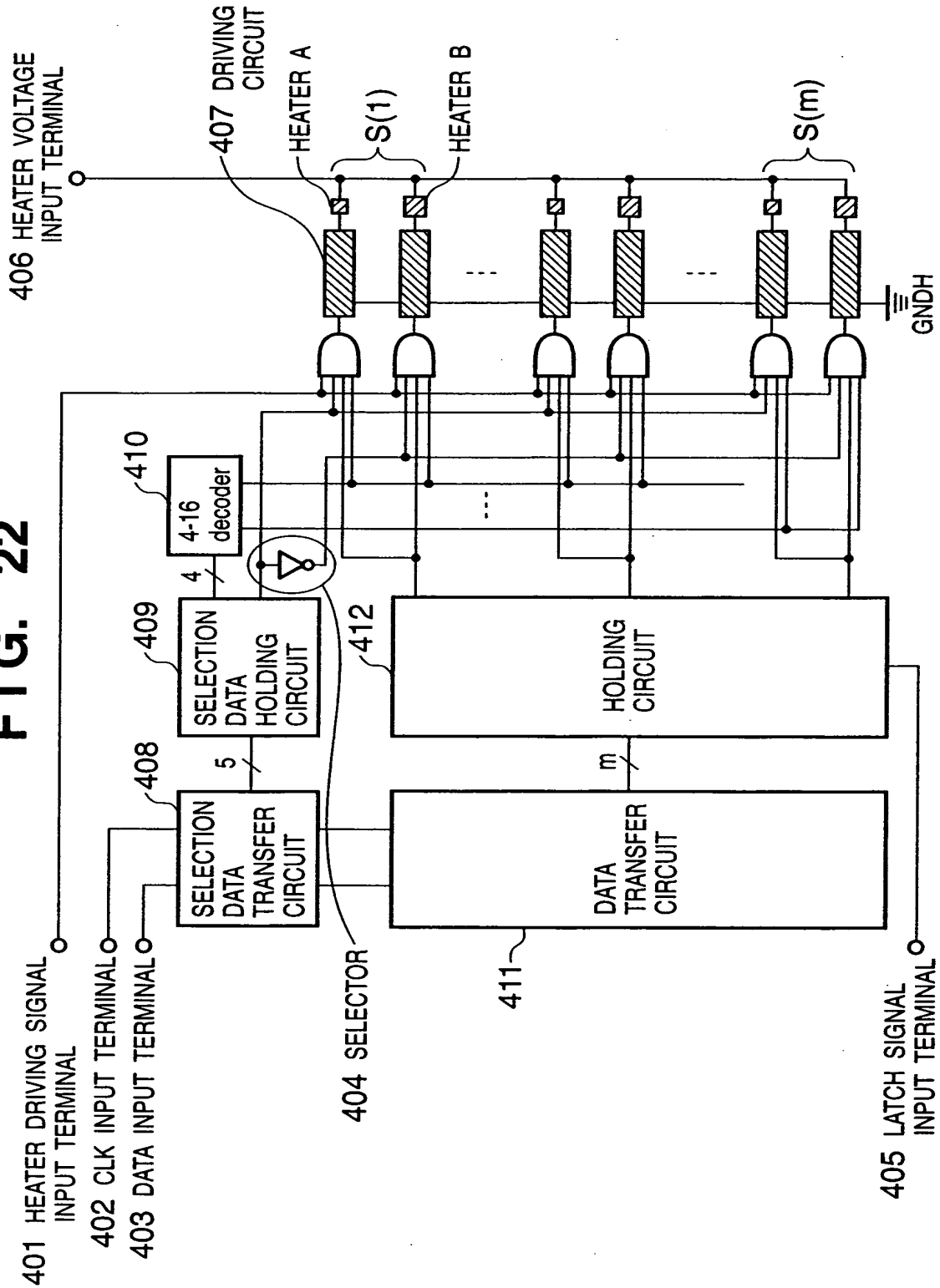
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FIG. 21



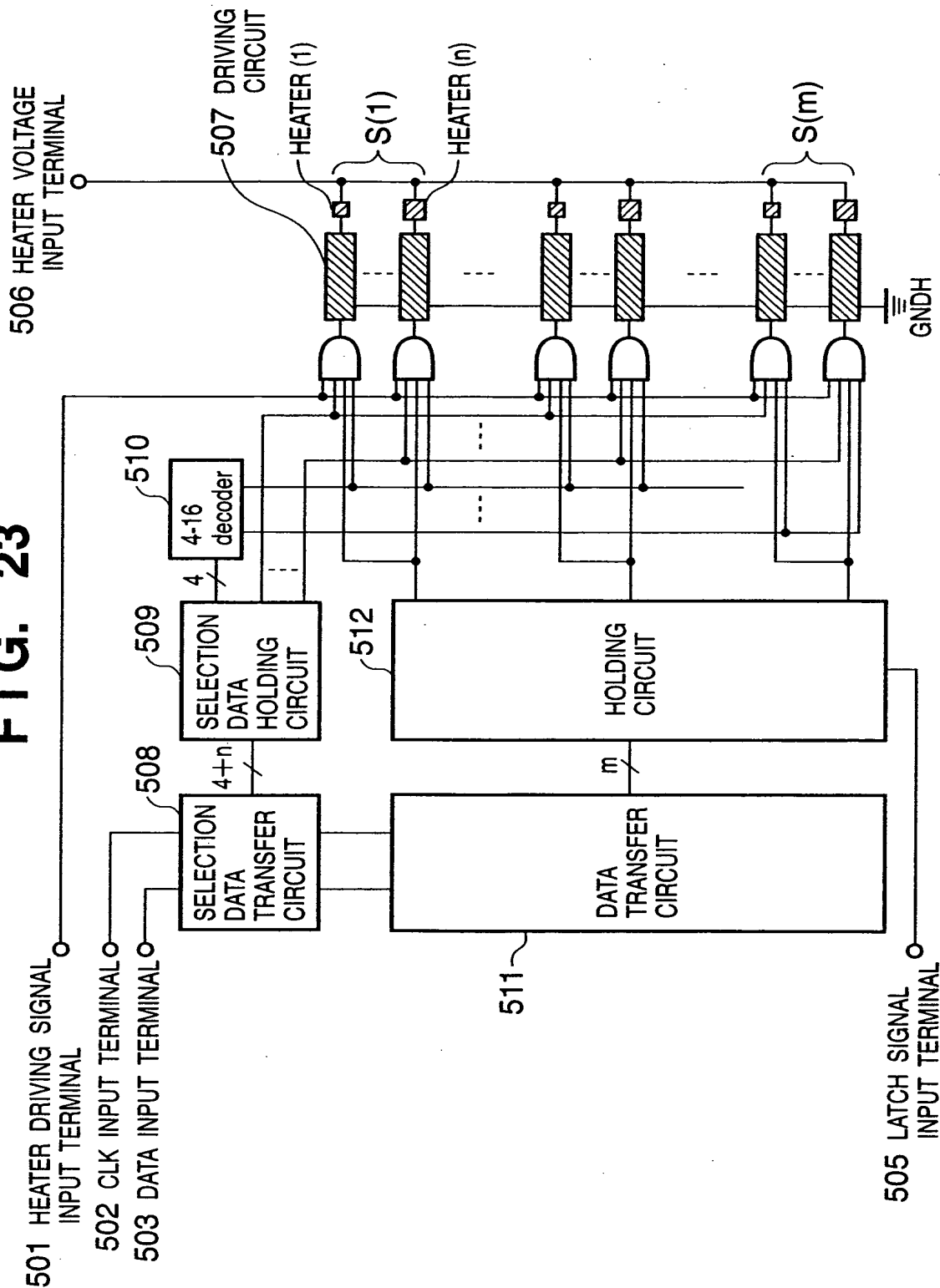
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FIG. 22



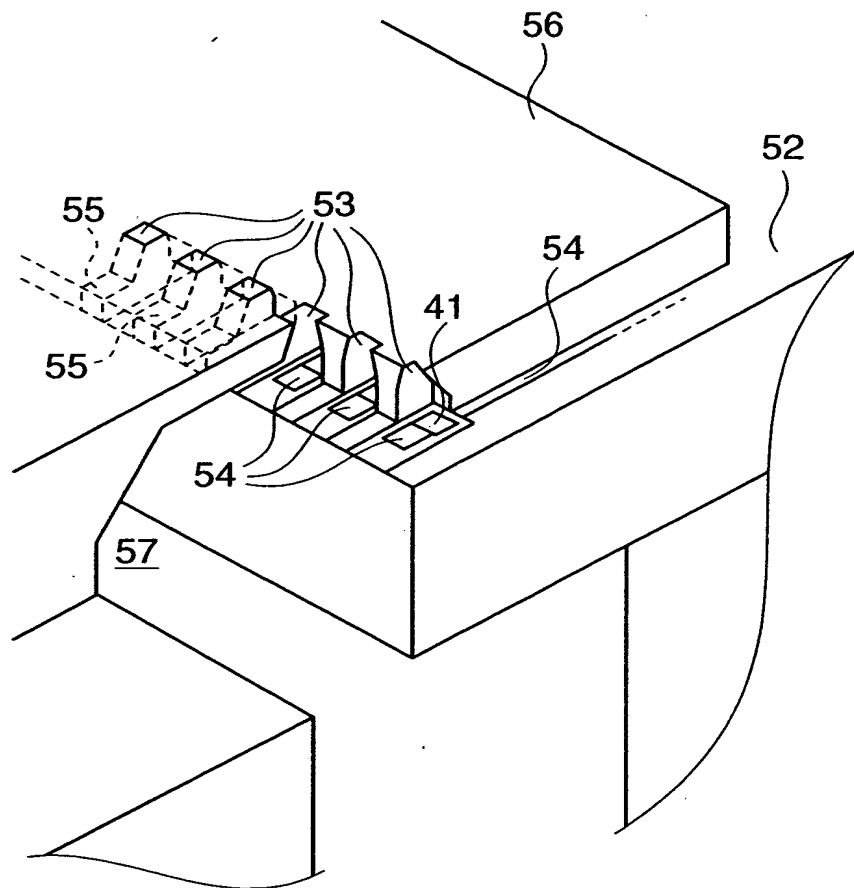
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FIG. 23



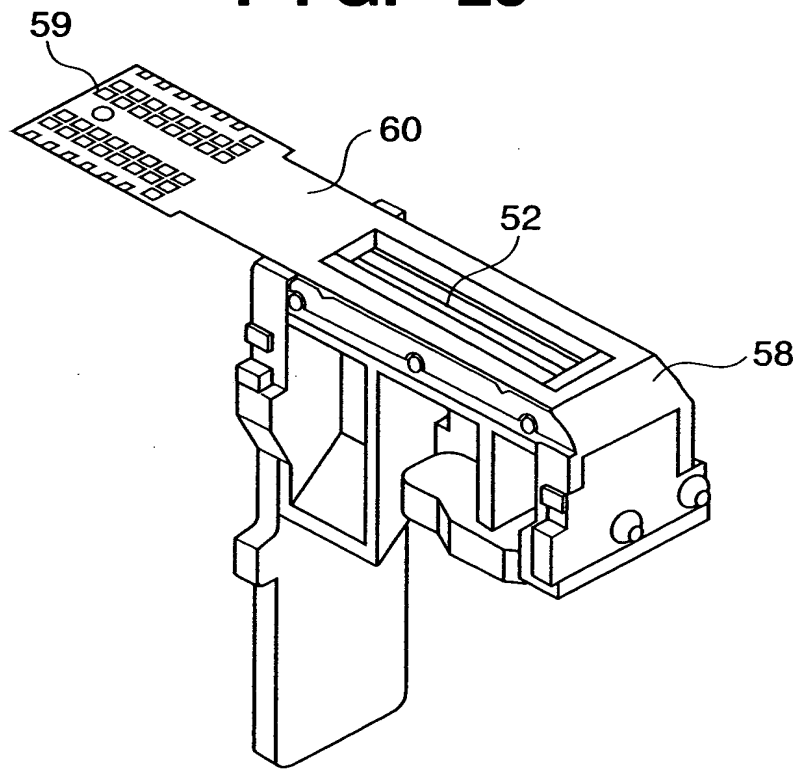
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FIG. 24



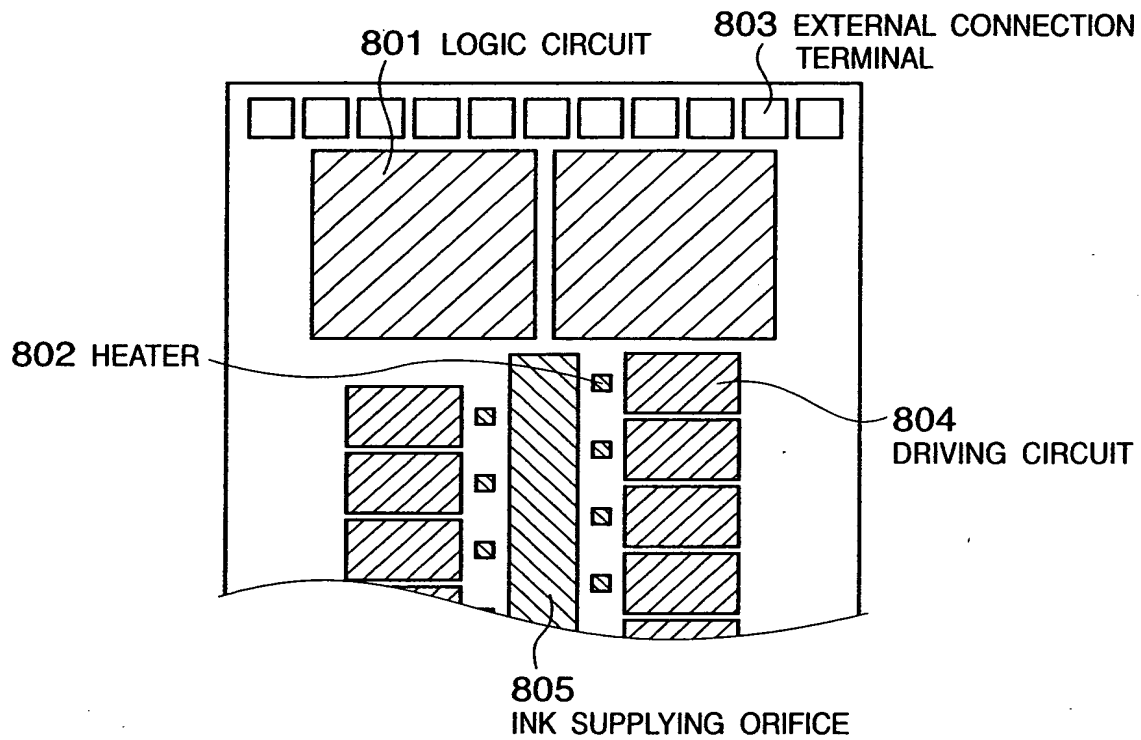
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FIG. 25



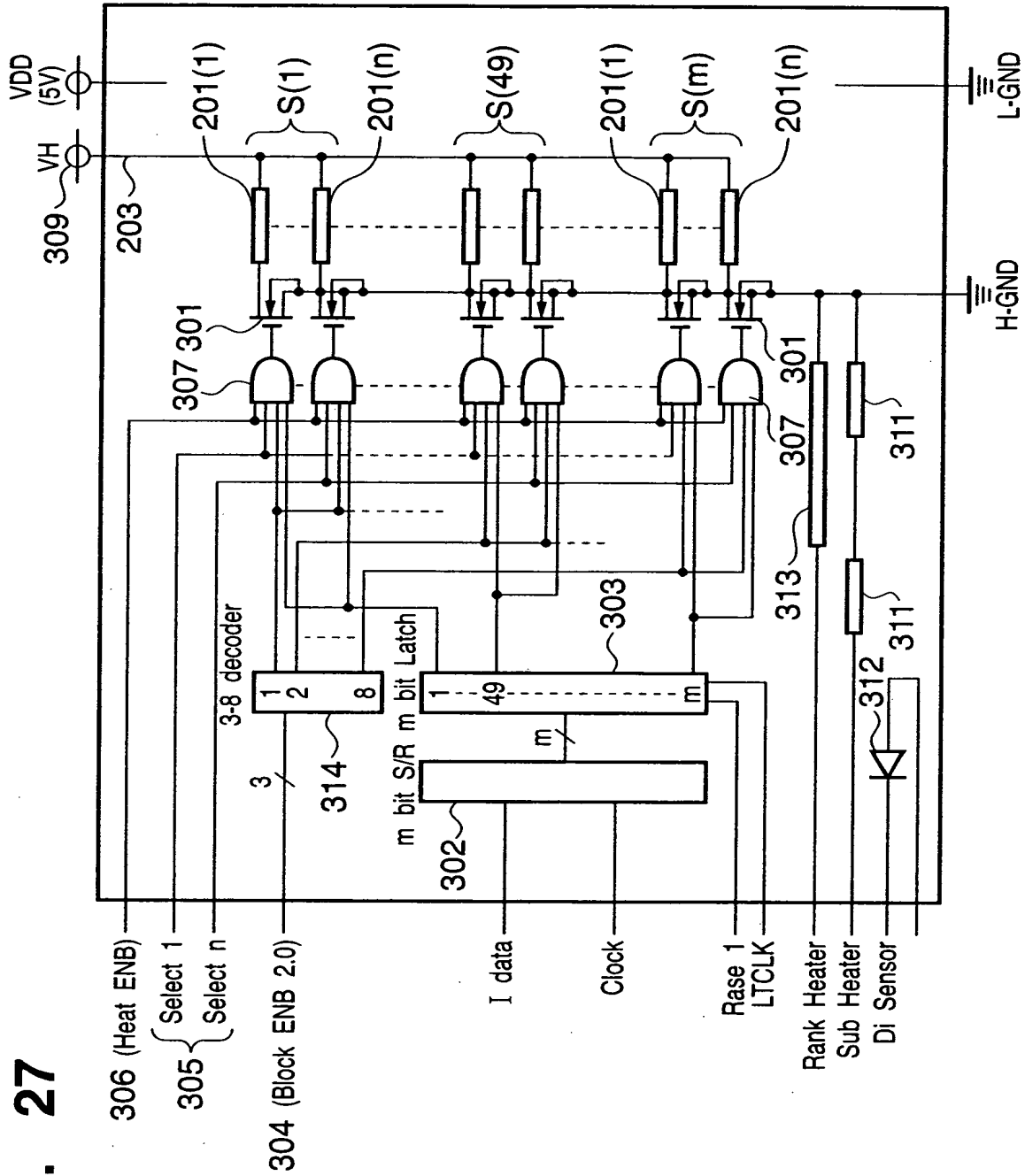
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FIG. 26



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FIG. 27



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FIG. 28

